

1 **ABSTRACT**

2 A flash driver architecture openly compatible to operate as interface  
3 between most types of file systems and flash memory media regardless of the  
4 manufacturer. The flash driver includes a flash abstraction logic that serves as a  
5 manager for operating characteristics that are common to the plurality of different  
6 types flash memory media. The flash driver may also include a programmable  
7 flash medium logic that performs more specific operations in direct  
8 communication with the flash memory medium. A user/manufacture of a  
9 computer device can optimally select a set of programmable entry points  
10 associated with the flash medium logic to ensure congruent and seamless  
11 operation between the file system and flash memory medium selected by the  
12 user/manufacture of the computer.  
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